

Day : Tuesday
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PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = JUNG

First Name = TAE-SUNG

Application#	Patent#	Status	Date Filed	Title	Inventor Name 27
<u>10278486</u>	Not Issued	030	10/23/2002	METHOD FOR TRANSMITTING DATA FROM SERVER OF VIRTUAL PRIVATE NETWORK TO MOBILE NODE	JUNG, TAE-SUNG
<u>10246744</u>	6603686	150	09/19/2002	SEMICONDUCTOR MEMORY DEVICE HAVING DIFFERENT DATA RATES IN READ OPERATION AND WRITE OPERATION	JUNG, TAE-SUNG
<u>10187700</u>	Not Issued	030	07/02/2002	METHOD OF TRANSMITTING DATA FROM SERVER OF VIRTUAL PRIVATE NETWORK TO MOBILE NODE	JUNG, TAE-SUNG
<u>10035466</u>	Not Issued	030	10/26/2001	HANDOVER METHOD FOR MOBILE STATION HAVING MOBILE IP ADDRESS IN MOBILE COMMUNICATION SYSTEM	JUNG, TAE-SUNG
<u>10002534</u>	Not Issued	030	11/01/2001	SYSTEM AND METHOD FOR ASSIGNING A MOBILE IP TO A MOBILE NODE	JUNG, TAE-SUNG
<u>09990567</u>	Not Issued	030	11/21/2001	REGIONAL TUNNEL MANAGEMENT METHOD IN A MOBILE COMMUNICATION SYSTEM USING MOBILE IP	JUNG, TAE-SUNG
<u>09941091</u>	Not Issued	030	08/28/2001	INTEGRATED CIRCUIT MEMORY SYSTEMS HAVING PROGRAMMABLE SIGNAL BUFFERS FOR ADJUSTING SIGNAL TRANSMISSION DELAYS AND METHODS OF OPERATING SAME	JUNG, TAE-SUNG
<u>09930973</u>	6477110	150	08/17/2001	SEMICONDUCTOR MEMORY DEVICE HAVING DIFFERENT	JUNG, TAE-SUNG

				DATA RATES IN READ OPERATION AND WRITE OPERATION	
<u>09918095</u>	Not Issued	030	07/30/2001	PACKET TRANSMISSION METHOD FOR MOBILE INTERNET	JUNG, TAE-SUNG
<u>09886552</u>	<u>6442057</u>	150	06/21/2001	MEMORY MODULE FOR PREVENTING SKEW BETWEEN BUS LINES	JUNG, TAE-SUNG
<u>09851277</u>	Not Issued	041	05/08/2001	MEMORY INTERFACE SYSTEMS THAT COUPLE A MEMORY TO A MEMORY CONTROLLER AND ARE RESPONSIVE TO A TERMINAL VOLTAGE THAT IS INDEPENDENT OF SUPPLY VOLTAGES FOR THE MEMORY AND THE MEMORY CONTROLLER	JUNG, TAE-SUNG
<u>09845550</u>	<u>6585525</u>	150	04/30/2001	MEMORY MODULES HAVING CONDUCTORS AT EDGES THEREOF AND CONFIGURED TO CONDUCT SIGNALS TO OR FROM THE MEMEORY MODULES VIA THE RESPECTIVE EDGES	JUNG, TAE-SUNG
<u>09514763</u>	<u>6275100</u>	150	02/28/2000	REFERENCE VOLTAGE GENERATORS INCLUDING FIRST AND SECOND TRANSISTORS OF SAME CONDUCTIVITY TYPE AND AT LEAST ONE SWITCH	JUNG, TAE-SUNG
<u>09082297</u>	<u>6044017</u>	150	05/19/1998	NONVOLATILE MEMORY DEVICE HAVING FLASH EEPROM CELLS	JUNG , TAE-SUNG
<u>08990023</u>	<u>5963475</u>	150	12/12/1997	ADVANCED NONVOLATILE MEMORIES ADAPTABLE TO DYNAMIC RANDOM ACCESS MEMORIES AND METHODS OF OPERATING THEREIN	JUNG , TAE-SUNG
<u>08927606</u>	<u>6040735</u>	150	09/12/1997	REFERENCE VOLTAGE GENERATORS INCLUDING FIRST AND SECOND TRANSISTORS OF SAME CONDUCTIVITY TYPE	JUNG , TAE-SUNG
<u>08769588</u>	<u>6012122</u>	150	12/18/1996	SYSTEMS AND METHODS FOR DISTINGUISHING BETWEEN	JUNG , TAE-SUNG

				MEMORY TYPES	
<u>08739276</u>	<u>5732018</u>	150	10/29/1996	SELF-CONTAINED REPROGRAMMING NONVOLATILE INTEGRATED CIRCUIT MEMORY DEVICES AND METHODS	JUNG , TAE-SUNG
<u>08716022</u>	<u>5677873</u>	150	09/19/1996	METHODS OF PROGRAMMING FLASH EEPROM INTEGRATED CIRCUIT MEMORY DEVICES TO PREVENT INADVERTENT PROGRAMMING OF NONDESIGNATED NAND MEMORY CELLS THEREIN	JUNG , TAE-SUNG
<u>08686423</u>	<u>5771192</u>	150	07/26/1996	BIT LINE REFERENCE CIRCUIT FOR A NONVOLATILE SEMICONDUCTOR MEMORY DEVICE	JUNG , TAE-SUNG
<u>08663350</u>	<u>5761123</u>	150	06/13/1996	SENSE AMPLIFIER CIRCUIT OF A NONVOLATILE SEMICONDUCTOR MEMORY DEVICE	JUNG , TAE-SUNG
<u>08650399</u>	<u>5796273</u>	150	05/20/1996	SENSE AMPLIFIER FOR SEMICONDUCTOR MEMORY DEVICE	JUNG , TAE-SUNG
<u>08638100</u>	<u>5737258</u>	150	04/26/1996	NONVOLATILE SEMICONDUCTOR MEMORY WHICH IS CONNECTABLE TO A DRAM BUS	JUNG , TAE-SUNG
<u>08623793</u>	<u>5621684</u>	150	03/29/1996	A NONVOLATILE SEMICONDUCTOR MEMORY WITH DIFFERENT PASS POTENTIAL APPLIED TO THE FIRST TWO ADJACENT WORD LINES	JUNG , TAE-SUNG
<u>07292449</u>	<u>4964084</u>	150	12/30/1988	STATIC RANDOM ACCESS MEMORY DEVICE WITH VOLTAGE CONTROL CIRCUIT	JUNG , TAE-SUNG
<u>07289731</u>	<u>4890051</u>	150	12/27/1988	CMOS INPUT BUFFER STABLE FOR THE VARIATION OF A POWER SUPPLYING VOLTAGE	JUNG , TAE-SUNG
<u>07238247</u>	<u>5067109</u>	150	08/30/1988	A DATA OUTPUT BUFFER CIRCUIT FOR A SRAM	JUNG , TAE-SUNG

Inventor Search Completed: No Records to Display.